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APPLICANTS Tadashi Iguchi, Yokkaichi-Shi, JAPAN; Yoshiaki Himeno, Yokkaichi-Shi, JAPAN; Hiroaki Tsunoda, Yokkaichi-Shi, JAPAN;				
** CONTINUING DATA ***** This application is a DIV of 09/732,723 12/11/2000 PAT 6,720,610				
** FOREIGN APPLICATIONS ***** JAPAN 11-350841 12/09/1999				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 02/27/2004				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Acknowledged <i>[Signature]</i> Allowance Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 22	TOTAL CLAIMS 9
				INDEPENDENT CLAIMS 3
ADDRESS 22850				
TITLE Method of manufacturing a nonvolatile semiconductor memory device having a stacked gate structure				
FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	

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